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ent Application TO-413), Int/Comment of Reasons for Allowance Our Payne RY PATENT EXAMINER

DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Kevin L. Smith on 9/20/2007.

The application has been amended according to the faxed claims (copy attached), sent to the examiner by Mr. Kevin L. Smith (Reg. No. 38,620) on 9/6/2007. In the claims:

Claim 20, line 1, 'The RFIC of claim 18' has been changed to ---The RFIC of claim 17---.

Allowable Subject Matter

- 2. Claims (1-11, 17, 20, and 24-26) are allowed.
- 3. The following is an examiner's statement of reasons for allowance: The art of record does not suggest the respective4 claim combinations together and nor would the respective claim combinations be obvious with:
- 4. Re claim 1, the further limitation of, "a radio frequency integrated circuit (RFIC) comprises: transmitter section operably coupled to convert outbound baseband signals into outbound radio frequency (RF) signals; receiver section operably coupled to convert inbound RF signals into inbound baseband signals, wherein the receiver section

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includes: a low noise amplifier operably coupled to amplify the inbound RF signals to produce amplified inbound RF signals; down-conversion module operably coupled to convert the amplified inbound RF signals into baseband in-phase components and quadrature components; orthogonal-normalizing module operably coupled to: obtain a first coefficient that is based on at least one of power of the baseband in-phase components, power of the baseband quadrature components, and cross-correlation between the baseband in-phase components and the baseband quadrature components; obtain a second coefficient that is based on at least one of the power of the baseband in-phase components, the power of the baseband quadrature components, and the cross-correlation between the baseband in-phase components and the baseband quadrature components; normalize an orthogonal relationship between the baseband in-phase components and the baseband quadrature components based on the first coefficient and the second coefficient to produce normalized in-phase components and normalized quadrature components; and baseband processor operably coupled to recapture data from the normalized in-phase and quadrature components". Claims 2-11 depend on claim 1.

5. Re claim 17, the further limitation of, "a radio frequency integrated circuit (RFIC) comprises: receiver section operably coupled to convert inbound radio frequency (RF) signals into inbound baseband signals; transmitter section operably coupled to convert outbound data into outbound RF signals, wherein the transmitter section includes: baseband processor operably coupled to convert the outbound data into the baseband in- phase components and baseband quadrature components; orthogonal-normalizing

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module operably coupled to: obtain a first coefficient that is based on at least one of a gain imbalance and phase imbalance; obtain a second coefficient that is based on at least one of the gain imbalance and the phase imbalance; normalize an orthogonal relationship between the baseband in-phase components and the baseband quadrature components based on the first coefficient and the second coefficient to produce normalized in-phase components and normalized quadrature components; upconversion module operably coupled to convert the normalized in-phase components and normalized quadrature components into RF signals; and power amplifier operably coupled to amplify the RF signals to produce the outbound RF signal; wherein the orthogonal-normalizing module comprises: a first multiplier module operably coupled to multiple the baseband in-phase components with the first coefficient to produce the normalized in-phase components; wherein the first multiplier module comprises: a first plurality of shift registers operably coupled to produce a plurality of shifted representations of the baseband in-phase components; switch matrix operably coupled to pass selected ones of the plurality of shifted representations of the baseband inphase components and the baseband in-phase components based on the first coefficient; and an adder operably coupled to add the selected ones of the plurality of shifted representations of the baseband in-phase components and the baseband inphase components to produce the normalized in- phase components; a second multiplier module operably coupled to multiple the baseband in-phase components with the second coefficient to produce cross coupled in-phase components; and a subtraction module operably coupled to subtract the cross coupled in-phase

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components from the baseband quadrature components to produce the normalized quadrature components". Claims 20 and 24-26 depend on claim 17 above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leon Flores whose telephone number is 571-270-1201. The examiner can normally be reached on Mon-Fri 7-5pm Alternate Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Payne can be reached on 571-272-3024. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair,-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a

USPTO Customer Service Representative or access to the automated information

system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

LF

September 10, 2007

DAVID C. PAYNE SUPERVISORY PATENT EXAMINER